



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/647,651	08/26/2003	Pavel Peleska	449122061700	2126

25227 7590 06/23/2005

MORRISON & FOERSTER LLP
1650 TYSONS BOULEVARD
SUITE 300
MCLEAN, VA 22102

EXAMINER

DANG, KHANH

ART UNIT PAPER NUMBER

2111

DATE MAILED: 06/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/647,651

Applicant(s)

PELESKA, PAVEL

Examiner

Khanh Dang

Art Unit

2111

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-8 is/are allowed.
- 6) ☒ Claim(s) 9, 10 and 12 is/are rejected.
- 7) ☒ Claim(s) 11 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Drawings

The drawings are objected to because they contain foreign language. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

Art Unit: 2111

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 9, 10, and 12 are rejected under 35 U.S.C. 102(b) as being anticipated by Young.

As broadly drafted, these claims do not define any structure/step that differs from Young.

With regard to claim 9, Young discloses a method for correcting faults in connections between digital modules (memory module 10 and redundant circuit memory 26, see at least Fig. 1 and description thereof), comprising: forming a connection by a first group of active connection lines (in Young, the so-called "first group of active connection lines" is defined by the connection lines connecting the segments 12, each comprising sixteen regular or first columns 14 of bit cells, to the redundant memory circuit 26) and providing a second group of inactive connection lines accordingly (in Young, the so-called "second group of inactive connection lines" is defined by the connection lines connecting to the spare/redundant or second columns 28, 30 of bit cells); and activating an inactive connection line of the second group and deactivating a connection line that has been active up until this point if the active connection line is found to be faulty by the control logic device (in Young, a multiplexer is provided for multiplexing conventional bit segments with spare columns of bit cells, wherein the spare columns are only activated, that is, selected, when a particular

Art Unit: 2111

column in the conventional bit segment has been identified to be defective, see at least the abstract), wherein the control logic device in cooperation with a multiplexing device (multiplexer in Young) controls activation and deactivation.

With regard to claim 10, Young discloses a circuit arrangement for correcting faults on connections between digital modules (memory module 10 and redundant circuit memory 26, see at least Fig. 1 and description thereof), comprising: a control logic device to detect arrangement-internal and arrangement-external faults of input/output connections and a multiplexer device to switch over data transmission of faulty active input/output connections to fault-free inactive input/output connections (Young discloses connection lines connecting the segments 12, each comprising sixteen regular or first columns 14 of bit cells, to the redundant memory circuit 26; and connection lines connecting to the spare/redundant or second columns 28, 30 of bit cells. Young further discloses a multiplexer for multiplexing conventional bit segments with spare columns of bit cells, wherein the spare columns are only activated, that is, selected, when a particular column in the conventional bit segment has been identified to be defective. See at least the abstract).

With regard to claim 12, it is clear from Young that the circuit arrangement is part of an integrated circuit.

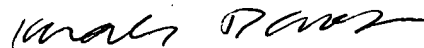
Allowable Subject Matter

Claims 1-8 are allowed.

Claim 11 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

US. Patent Nos. 6,642,733 to Peleska et al., 5,313,424 to Adams et al., 6,085,334 to Giles, 6,590,816 to Perner, 5,577,050 to Bair et al., and US 2003/0051086 are cited as relevant art.

Any inquiry concerning this communication should be directed to Khanh Dang at telephone number 703-308-0211.



**Khanh Dang
Primary Examiner**